

## CURRICULUM VITAE

Personal Information	
<b>Name</b>	Leonid Djinevski
<b>Phone</b>	+389 70 954 623
<b>E-mail</b>	l.djinevski@utms.edu.mk
<b>Nationality</b>	Macedonian
Date of Birth	16.11.1982
Gender	Male
Academic Title	PhD in technical sciences

Work Experience	
• Dates (from - to)	2023 – present
• Name and address of employer	University of Skopje
• Type of business or sector	Higher Education and Research
• Job Title	Professor
• Main activities and responsibilities	Educational process on courses: Introduction to programming, Visual Programming, Web Programming, Object-oriented programming, High performance processing and others.
• Dates (from - to)	2012 – 2023
• Name and address of employer	Fon University
• Type of business or sector	Higher Education and Research
• Job Title	Professor
• Main activities and responsibilities	Educational process on courses: Architecture of computer systems, Visual programming, Network operation systems, Parallel processing and others.
• Dates (from - to)	2010-2012
• Name and address of employer	Ss Cyril and Methodius University in Skopje, Macedonia
• Type of business or sector	Higher Education and Research
• Job Title	Research Assistant
• Main activities and responsibilities	Research in the field of parallel processing and network simulation
• Dates (from - to)	2008 – 2010
• Name and address of employer	Instituto Superior Tecnico, Lisbon, Portugal
• Type of business or sector	Higher Education and Research
• Job Title	Research Assistant
• Main activities and responsibilities	Parallel processing, microelectronics and control. Developing bootloader for microcontrollers in autonomous underwater vehicle Developing RS-232 interface for LASER distance sensor and developing nonlinear controller for a real quadrotor autonomous flying vehicle.
• Dates (from - to)	2007
• Name and address of employer	Carnegie Mellon University, Pittsburg, USA
• Type of business or sector	Higher Education and Research
• Job Title	Research Assistant
• Main activities and responsibilities	Image processing, implementing spiral.net algorithms to JPEG2000

• Dates (from - to)	02.04.2016
• Name and address of employer	Ulster University, Colrairie, Northern Ireland, United Kingdom
• Type of business or sector	Higher Education and Research
• Job Title	Project Officer
• Main activities and responsibilities	Web services and application for researchers

Education and training	
• Dates (from - to)	2010 – 2013
• Name and type of organization providing education or training	Ss. Cyril and Methodius University in Skopje, FINKI
• Principal subjects / occupational skills covered in the training	Technical Sciences
• Assigned title	Phd in Technical Sciences
• Level in national classification	VIII

Professional Experience	
• Dates (from - to)	2021-2024
• Location	Skopje, Macedonia
• The company and the person responsible (name and contact details)	Sorsix
• Position	Project Tech Lead
• Description	Design and development of projects for medical software in Republic of Macedonia, Australia and USA. Starting from the POC, MVP, and leading the architecture to the production products. Design and Implementation of Azure infrastructure. Integration with Salesforce [api, bulk ETL]. Integration with third party services for sms, phone, mail, voicemail and agent tracking. Integration with Iguana - HL7 queue provider. .NET8/angular web app, modular design. WinForms app deployment on-premise and on third party partner's premises, as well as graceful over the air updates. Implementing CICD for the web app, winform app, and ADF pipelines. Productivity and Insight reports (rdl and powerBI). ETL process SSIS / ADF.
• Dates (from - to)	2020-2021
• Location	Skopje, Macedonia
• The company and the person responsible (name and contact details)	Synapse Aviation
• Position	Head of Development
• Description	Интерна веб апликација.NET/angular, AWS cloud serverless infrastructure using Lambda functions ,S3 as storage and CDM, aurora db and cloud formations.. iOS application with over the air updates developed in Unity3D. ETL process of decoding and transforming aeronautical ARINC 424 data in the

system.

### International congresses and symposia

• Dates (from – to)	September 19 - October 1, 2022
• Location	Skopje, Macedonia
• The company and the person responsible	Association for Information and Communication Technologies ICT-ACT - ICT Innovations 2022, Web Proceedings, Chairs: Prof. Katerina Zdravkova, PhD and Prof. Lasko Basnarkov, PhD
• Position	Co-author Markovski, Gjorgji, Leonid Djinevski, Slavcho Chungurski, and Toni Stojanovski. "Aeronautical Object Recognition using Neural Network Algorithms." 2022.
• Dates (from – to)	27 September - 01 October 2021
• Location	Opatija, Croatia
• The company and the person responsible	2021 44th International Convention on Information, Communication and Electronic Technology (MIPRO)
• Position	Co-author Mitrovski, Jakov, Leonid Djinevski, Marjan Gusev, and Sime Arsenovski. "TPC-H Benchmark Q3, Q6 and Q12 Sequential, OpenMP Parallel and CUDA Parallel Implementation."
• Dates (from – to)	8 - 9 May 2020
• Location	Online due to COVID-19 pandemic
• The company and the person responsible	Ss. Cyril and Methodius University in Skopje, FINKI, Editors: Eftim Zdravevski and Petre Lameski, 17th International Conference on Informatics and Information Technologies - CIIT 2020
• Position	Co-author Jovevska, Zivka, Daniel Jovevski, and Leonid Djinevski. "Security Situation in Republic of Macedonia Using Semantic Algorithms for Open Data."
• Dates (from – to)	30 May – 3 June 2016
• Location	Opatija, Croatia
• The company and the person responsible	2016 39th International Convention on Information, Communication and Electronic Technology (MIPRO)
• Position	Co-author Jakimoski, K., Arsenovski, S., Gorachinova, L., Chungurski, S., Iliev, O., Djinevski, L., & Kamcheva, E. Evaluation of Caching Techniques for Video on Demand in Named Data Networks, MIPRO 2016 - DC VIS - DISTRIBUTED COMPUTING, VISUALIZATION AND BIOMEDICAL ENGINEERING, At Opatija, Croatia
• Dates (from – to)	1 – 4 October 2015
• Location	Ohrid, Macedonia

• The company and the person responsible	Association for Information and Communication Technologies ICT-ACT - ICT Innovations 2015, Web Proceedings, Chairs: Suzana Loshkovska, and Saso Koceski
• Position	Co-author P. Lameski, L. Pejov, L. Djinevski, D. Trajanov and S. Filiposka. "Sampling Rare Events in Hybrid Statistical Physics – Quantum Mechanical Studies of Hydrophobic Interactions in Liquids: A GPU Implementation"
• Dates (from – to)	12 – 14 September 2014
• Location	Ohrid, Macedonia
• The company and the person responsible	Association for Information and Communication Technologies ICT-ACT - ICT Innovations 2014, Web Proceedings, Chairs: Ana Madevska-Bogdanova and Dejan Gjorgjevikj
• Position	Co-author M. Ivanovska, L. Djinevski, S. Arsenovski, "Computational Module as a Tool for Financial Viability Analysis in Decision Making Process in Private University Institutions"
• Dates (from – to)	12-14 February 2014
• Location	Niš, Serbia
• The company and the person responsible	Proceedings of the 5th Small Systems Simulation Symposium 2014
• Position	Co-author Djinevski, L., Filiposka, S., Mishkovski, I., & Trajanov, D. Wireless Ad Hoc Network Simulation in Cloud Environment
• Dates (from – to)	26 – 28 November 2013
• Location	Belgrade, Serbia
• The company and the person responsible	21-th Telecommunications Forum (TELFOR 2013), IEEE Conference Proceedings
• Position	Co-author L. Djinevski, S. Filiposka, D. Trajanov, I. Mishkovski, "GPU performance impact of the Durkin's radio propagation algorithm"
• Dates (from – to)	12 – 15 September 2013
• Location	Ohrid, Macedonia
• The company and the person responsible	ICT Innovations 2013, Conference Web Proceedings (ISSN 1857-7288)
• Position	Co-author L. Djinevski, S. Arsenovski, S. Ristov, M. Gusev, "Optimal Configuration of GPU Cache Memory to Maximize the Performance"
• Dates (from – to)	9 – 11 September 2013
• Location	Kraków, Poland
• The company and the person responsible	Federated Conference on Computer Science and Information Systems (FEDCSIS 2013), IEEE Conference Proceedings

• Position	Co-author M. Gusev, S. Ristov, L. Djinevski, S. Arsenovski, “Performance Impact of Reconfigurable L1 Cache on GPU Devices”
• Dates (from – to)	20 – 24 May 2013
• Location	Opatija, Croatia
• The company and the person responsible	36th International Convention MIPRO, IEEE Conference Proceedings
• Position	Co-author L. Djinevski, S. Arsenovski, S. Ristov, M. Gusev, “Performance Drawbacks for Matrix Multiplication using Set Associative Cache in GPU devices”
• Dates (from – to)	16 – 19 March 2013
• Location	Kopaonik, Serbia
• The company and the person responsible	3rd International Conference on Internet Society Technology and Management (ICIST 2013)
• Position	Co-author S. Stojanova, L. Djinevski, I. Mishkovski, S. Filiposka, D. Trajanov, “Micro-benchmarking NS-2 and NS-3 Network Simulators Using Terrain Aware Radio Propagation Extension”
• Dates (from – to)	18 – 21 April 2013
• Location	Bitola, Macedonia
• The company and the person responsible	The 10th Conference for Informatics and Information Technology (CIIT 2013)
• Position	L. Djinevski et al, “Transforming computer lab into a mini HPC cluster”
• Dates (from – to)	18 – 21 April 2013
• Location	Bitola, Macedonia
• The company and the person responsible	The 10th Conference for Informatics and Information Technology (CIIT 2013)
• Position	Co-author P. Lameski, I. Mishkovski, S. Filiposka, D. Trajanov, L. Djinevski, “Comparison of rectangular matrix multiplication with and without border conditions”
• Dates (from – to)	12 – 14 February 2012
• Location	Niš, Serbia
• The company and the person responsible	, Proceedings of Small Systems Simulation Symposium 2012
• Position	Co-author L. Djinevski, S. Filiposka, D. Trajanov, “Network Simulator Tools and GPU Parallel Systems”
• Dates (from – to)	2009

• Location	Ohrid, Macedonia
• The company and the person responsible	ETAI Conference
• Position	L. Djinevski, D. Trajanov, Modeling and Simulation of a Quadrotor Helicopter
• Dates (from – to)	2009
• Location	Kopaonik, Serbia
• The company and the person responsible	YU INFO Conference 2009
• Position	Co-author L. Djinevski, D. Trajanov, Boot loading AVR microcontrollers through CAN
• Dates (from – to)	2003
• Location	Ohrid, Macedonia
• The company and the person responsible	ETAI Conference 2003
• Position	Author L. Djinevski, E-Learning Application and student interaction,.

Publications	
2024	Leonid Djinevski, ANALYSIS OF PARELLEL MULTIPROCESSOR EXECUTION OF DIFFERENT PROGRAMMING APPROACHES, KNOWLEDGE – International Journal Vol. 65, August 2024
2024	Leonid Djinevski CHATGPT INFLUENCE IN WORK ENVIRONMENT OF IT COMPANIES IN MACEDONIA, KNOWLEDGE – International Journal Vol. 65, August 2024.
2024	Leonid Djinevski PhD, Sime Arsenovski PhD, “COMPARATIVE OVERVIEW OF INTEROPERABILITY IN DISTRIBUTED MEDICAL SYSTEMS THROUGH DATA EXCHANGE STANDARDS IN MACEDONIA, USA AND AUSTRAILA REGARDING HEALTH PROVIDERS AND INSURANCE AGENCIES”, UTMS Journal of Economics, Vol. 15 No. 1 (241-252), June 2024 (EBSCO)
2016	Jakimoski, K., Arsenovski, S., Gorachinova, L., Chungurski, S., Iliev, O., Djinevski, L., & Kamcheva, E. (2016). Measurements of Available Bandwidth in Computer Networks. International Journal of Grid and Distributed Computing, 9(4), 201-209
2015	L. Djinevski, S. Filiposka, I. Mishkovski, D. Trajanov, “Accelerating wireless network simulation in 3D terrain using GPUs”, Ad Hoc & Sensor Wireless Networks, (ISSN 1551-9899) 2015, Vol. 29 Issue 1-4, p253-264, (2015). (2015 Thomson Reuters Impact Factor: 0.44)
2014	L. Djinevski, S. Stojanova, D. Trajanov, “Optimizing Durkins Propagation Model Based on TIN Terrain Structures”, ICT Innovations 2013, (ISBN 978-3-319-01465-4) (ISSN 2194-5357) Springer Berlin Heidelberg, pp 263-272, 2013, (2014).
2013	L. Djinevski, S. Ristov, M. Gusev, “Superlinear Speedup for Matrix Multiplication in

	GPU Devices”, ICT Innovations 2012, (ISBN 978-3-642-37168-4) (ISSN 2194-5357) Springer Berlin Heidelberg, pp 285-294, ( <i>book chapter</i> 2013).
2011	L. Djinevski, I. Mishkovski, D. Trajanov, “Accelerating Clustering Coefficient Calculations on a GPU Using OPENCL”, ICT Innovations 2010, (ISBN 978-3-642-19324-8) (ISSN 1865-0929) Springer Berlin Heidelberg, pp 276-285, ( <i>book chapter</i> 2011).